

030104

17224 U.S. PTO

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****CERTIFICATE OF EXPRESS MAILING**

I hereby certify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service on March 1, 2004 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number **EV332826365US**, addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Attorney Docket No.: 03-2051/LSIIP240

First Named Inventor: PRITCHARD

22857 U.S. PTO  
10/791337

Jeffrey Ng

**UTILITY PATENT APPLICATION TRANSMITTAL (37 CFR. § 1.53(b))**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir: This is a request for filing a patent application under 37 CFR. § 1.53(b) in the name of inventors:  
**David Pritchard, Hemanshu Bhatt and David T. Price**

For: **SPACER-LESS TRANSISTOR INTEGRATION SCHEME FOR HIGH-K GATE DIELECTRICS AND SMALL GATE-TO-GATE SPACES APPLICABLE TO SI, SiGe AND STRAINED SILICON SCHEMES**

Assigned to: LSI Logic Corporation

Application Elements:

- ☒ **27** Pages of Specification, Claims and Abstract
- ☒ **09** Sheets of formal Drawings
- ☒ **03** Pages Combined Declaration and Power of Attorney (2 sets)

Accompanying Application Parts:

- ☐ Do not publish this application. Nonpublication Request is attached.
- ☒ Assignment and Assignment Recordation Cover Sheet
- ☐ 37 CFR 3.73(b) Statement by Assignee
- ☐ Information Disclosure Statement with Form PTO-1449
- ☐ Copies of IDS Citations
- ☐ Preliminary Amendment
- ☒ Return Receipt Postcard
- ☐ Other:

Claim For Foreign Priority

- ☐ Priority of \_\_\_\_\_ Application No. \_\_\_\_\_ filed on \_\_\_\_\_  
is claimed under 35 U.S.C. § 119.
- ☐ The certified copy is enclosed.
- ☐ The certified copy has been filed in prior application U.S. Application No. \_\_\_\_\_
- ☐ The certified copy will follow.

Fee Calculation (37 CFR § 1.16)

- ☐ Applicant is entitled to Small Entity Status under 37 C.F.R. §1.27.

	(Col. 1) Total Claims		(Col. 2) Claims	(Col. 3) Present Extra	Rate	Additional Fee
TOTAL	17	MINUS	20	= 0	x 18 =	\$-0-
INDEP.	2	MINUS	3	= 0	x 86 =	\$-0-
[ ] First presentation of multiple dependent claim					\$280 =	
Basic Filing Fee under 37 C.F.R. §1.16(a)					\$770 =	\$770.00
TOTAL						\$770.00
SMALL ENTITY 50% FILING FEE REDUCTION (if applicable)						

- ☒ The Commissioner is authorized to charge any fees which may be required to Deposit Account No. 12-2252 (Order No. 03-2051).

General Authorization for Petition for Extension of Time (37 CFR §1.136)

- ☒ Applicants hereby make and generally authorize any Petitions for Extensions of Time as may be needed for any subsequent filings. The Commissioner is also authorized to charge any extension fees under 37 CFR §1.17 as may be needed to Deposit Account No. 12-2252 (Order No. 03-2051).

- ☒ Please send correspondence to the following address:

**Customer Number 24319**  
LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milpitas, California 95035  
Telephone (408) 433-6100

Date: March 1, 2004

  
\_\_\_\_\_  
James E. Austin  
Registration No. 39,489